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TRANSMITTAL O	Docket No. 5-3 BUR920010175US1							
In Re Application Of: Timothy Lehner, et al.								
Serial No. 10/063,214	TAAUC							
Title: A CIRCUIT AND METHOD FOR MODELING I/O								
Address to: Assistant Commissioner for Patents Washington, D.C. 20231 37 CFR 1.97(b)								
1. The Information Disclosure Statement submitted herewith is being—filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.								
 The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of: 								
☐ the statement specified in 37 CFR 1.97(e);								
☐ the fee se	OR et forth in 37 CFR 1.17(p).							

TRANSMITTAL OF INFORMATION DISCLOSURE STATE (Under 37 CFR 1.97(b) or 1.97(c))				Docket No. BUR920010175US1				
In Re Application: Timothy Lehner, et al. MAY 0 6 2002 TC 2380 MAIL ROPM								
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		Paymen	t of Fee					
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Docket Number (Optional) Application Number BUR920020010175US1 10/063,214 INFORMATION DISCLOSURE CITATION Applicant(s) Timothy Lehner, et al. (Use several sheets if necessary) MAY 0 6 2002 Filing Date Group Art Unit 03/29/02 & TRADEMAN U.S. PATENT DOCUMENTS EXAMINER FILING DATE REF DOCUMENT NUMBER DATE NAME CLASS SUBCLASS INITIAL IF APPROPRIATE 5,481,484 01/02/96 Ogawa et al. 5,535,146 07/09/96 Huang et al. 5,652,716 07/29/97 Battersby et al. 5,675,502 10/07/97 Cox 5,696,694 12/09/97 Khouja et al. 5,706,477 01/06/98 Goto 5,757,679 05/26/98 Sawai et al. 5,838,947 11/17/98 Sarin 5,920,489 07/06/99 Dibrino et al. 5,949,983 09/07/99 Baxter 6,005,829 12/21/99 Conn FOREIGN PATENT DOCUMENTS Translation REF DOCUMENT NUMBER DATE COUNTRY CLASS SUBCLASS YES NO OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) Bina Ackalloor, Dinesh Gaitonde, "An Overview of Library Characterization in Semi-Custom Design" 5/97 IEEE 1998 Custom Integrated Circuits Conference, pp. 305-312 Jerry D. Hayes and Larry Wissel, "Behavioral Modeling for Timing, Noise, and Signal Integrity Analyis" **IBM Microelectroinics Division** EXAMINER DATE CONSIDERED

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considered. Include copy of this form with next communication to applicant.

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not

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		TRADE	TENT	DOCUMENTS			-		
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME		CLASS	SUBCLASS FILING DATE IF APPROPRIATE			
	6,035,115	03/07/00	Suzuki						
	6,080,201	06/27/00	Hojat, et al.						
	6,102,960	08/15/00	Berman, et al.						
	6,110,219	08/29/00	Jiang						
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	OTHER DOCUM	ENTS (Includin	g Author	, Title, Date, Pertinent	Pages, Etc	.)			
	Chandrakasan et al., ISBN 0-7803-6001-X,	'Design of High-P IEEE Order No. F	erformanc	ce Microprocessor Circuit hapter 16.3 pp. 338-345	311				
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	үү. 1320-1333								
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